

Power ON Character	Associated Test
A	Boot Processor (BP) Tests
a	AP Init
b	BP to AP Interrupt Test
c	AP to BP Interrupt Test
d	AP Halt
B	Application Processor (AP) Tests
C	Determine SP Personality
D	ECC Tests
a	Single Bit ECC test
b	Multi Bit ECC test
E	NVRAM Tests
a	Initialize NVRAM
b	Address Tests
c	Pattern Tests
d	Resume PROM Test
F	Real Time Clock (RTC) Test
G	Console UART Register Test
H	SPS UART Tests
a	Register Test
b	Loopback Interrupt Test
I	LCC 0 UART Tests
a	Register Test
b	Loopback Interrupt Test
c	FRUMON Test
J	LCC 1 UART Tests
a	Register Test
b	Loopback Interrupt Test
c	FRUMON Test
K	LCC 2 UART Tests
a	Register Test
b	Loopback Interrupt Test
c	FRUMON Test
L	LCC 3 UART Tests
a	Register Test
b	Loopback Interrupt Test
c	FRUMON Test
M	Ethernet Tests
a	Register Test
b	Interrupt Test
c	Loopback Packet Test

N	Back End 0 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
O	Back End 1 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
P	Peer Path 0 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
Q	Peer Path 1 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
R	Spare 0 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
S	Spare 1 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
T	Front End 0 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
U	Front End 1 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
V	Front End 2 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
W	Front End 3 FCC Tests
a	Register Test
b	Hierarchical Loopback Test
X	POST ROM
Y	BIOS ROM
Z	All FCC Tests
AA	Autoflash